

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Stephen W. Bedell, et al.

Examiner: Unassigned

Serial No: Unassigned

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(14710A)

For: METHOD OF CREATING
HIGH-QUALITY RELAXED
SiGe-ON-INSULATOR FOR STRAINED
Si CMOS APPLICATIONS

Dated: April 14, 2004

Mail Stop Patent Application
Commissioner for Patents
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

In connection with filing the above-identified application under 37 C.F.R.

§1.53(b), applicants submit the following amendments and remarks for consideration by the Examiner and entry of record in the above-identified patent application.

CERTIFICATE OF MAILING BY "EXPRESS MAIL"

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Date of Deposit: April 14, 2004

I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. §1.10 on the date indicated above and is addressed to the Mail Stop Patent Application, Commissioner of Patents and Trademarks, Alexandria, VA 22313-1450.

Dated: April 14, 2004



Leslie S. Szivos, Ph.D.